

NOT RECOMMENDED FOR NEW DESIGN USE DMN65D8LDW



2N7002DWA

DUAL N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(ON)}	Package	I _D T _A = +25°C	
60V	$8\Omega @ V_{GS} = 5V$	SOT363	170mA	
000	6Ω @ V _{GS} = 10V	301303	200mA	

Description

This new generation MOSFET is designed to minimize the on-state resistance $(R_{DS(ON)})$ and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

Applications

- DC-DC Converters
- Power Management Functions
- Battery Operated Systems and Solid-State Relays
- Drivers: Relays, Solenoids, Lamps, Hammers, Displays,

Memories, Transistors, etc

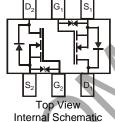




SOT363



Top View



Features

- Dual N-Channel MOSFET
- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Small Surface Mount Package
- HBM Class 1C
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: SOT363
- Case Material: Molded Plastic.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish Annealed over Alloy 42 Leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208 (2)
- Terminal Connections: See Diagram
- Weight: 0.006 grams (approximate)

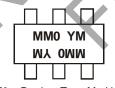
Ordering Information (Note 4 & 5)

Part Number	Compliance	Case	Packaging
2N7002DWA-7	Standard	SOT363	3,000/Tape & Reel
2N7002DWA-13	Standard	SOT363	10,000/Tape & Reel
2N7002DWAQ-7	Automotive	SOT363	3,000/Tape & Reel
2N7002DWAQ-13	Automotive	SOT363	10,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to http://www.diodes.com/quality/product_compliance_definitions/.
- 5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



MM0 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: Z = 2012) M = Month (ex: 9 = September) MM1 YM
WA LWW

MM1 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: Z = 2012) M = Month (ex: 9 = September) MM4 YM WA ÞWW

MM4 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: Z = 2012) M = Month (ex: 9 = September)

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Date Code Key

Code

Year	2012	2013	2014	2015	2016	2017	2018
Code	Z	Α	В	С	D	Е	F
Month	Jan Feb	Mar	Apr Mav	Jun Jul	Aug Se	p Oct	Nov Dec



Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

	Characteristic			Symbol	Value	Units
Drain-Source Voltage				V _{DSS}	60	V
Gate-Source Voltage				V _{GSS}	±20	V
Continuous Drain Current (Note 6)	V _{GS} = 10V	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	180 140	mA
Continuous Drain Current (Note 6)	$V_{GS} = 5V$	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	ID	150 120	mA
Continuous Drain Current (Note 7)	V _{GS} = 10V	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	200 160	mA
Continuous Drain Current (Note 7)	V _{GS} = 5V	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	170 140	mA
Pulsed Drain Current (10µs pulse, dut	ty cycle = 1%)	·		I _{DM}	700	mA

Thermal Characteristics

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 6)	P _D	300	mW
Thermal Resistance, Junction to Ambient (Note 6)	Reja	435	°C/W
Total Power Dissipation (Note 7)	P_D	400	mW
Thermal Resistance, Junction to Ambient (Note 7)	R _{θJA}	330	°C/W
Thermal Resistance, Junction to Case (Note 7)	Rejc	139	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

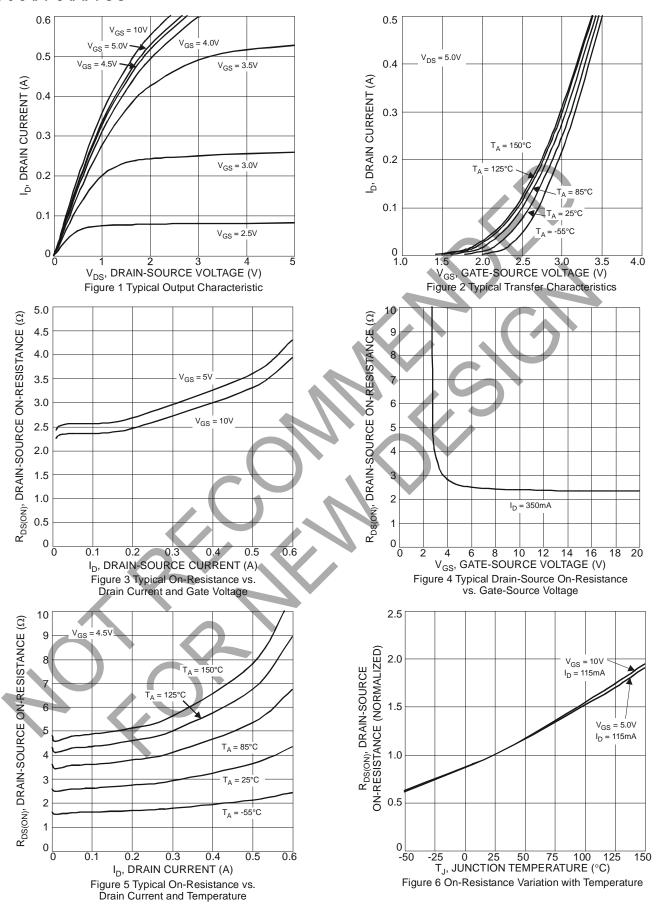
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

			_				
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV _{DSS}	60	_		V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	1		1.0	μΑ	$V_{DS} = 60V, V_{GS} = 0V$	
Gate-Body Leakage	I _{GSS}		_	±5	μΑ	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)		_					
Gate Threshold Voltage	V _{GS(th)}	0.8	_	2.5	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$	
Static Drain-Source On-Resistance		_		8	Ω	$V_{GS} = 5.0V, I_D = 0.115A$	
Static Dialit-Source Off-resistance	R _{DS} (ON)			6	Ω	$V_{GS} = 10.0V, I_D = 0.115A$	
Forward Transconductance	g FS	80	_		mS	$V_{DS} = 10V, I_D = 0.115A$	
Diode Forward Voltage	V_{SD}		0.8	1.2	V	$V_{GS} = 0V, I_{S} = 115mA$	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	C _{iss}		22.0				
Output Capacitance	Coss	1	3.2		pF	$V_{DS} = 25V, V_{GS} = 0V, f = 1.0MHz$	
Reverse Transfer Capacitance	C _{rss}		2.0	1			
Gate Resistance	R_{G}	1	88	1	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge V _{GS} = 10V	Q_g	-	0.87	-			
Total Gate Charge V _{GS} = 4.5V	Q_{g}	l	0.43	l	nC	$V_{GS} = 10V, V_{DS} = 30V,$	
Gate-Source Charge	Q _{gs}	1	0.11	1	110	$I_D = 150 \text{mA}$	
Gate-Drain Charge	Q_{gd}	1	0.11		<u></u>		
Turn-On Delay Time	t _{D(on)}		3.3				
Turn-On Rise Time	tr	1	3.2	1	nS	$V_{DD} = 30V, I_D = 0.115A, V_{GEN} = 10V,$	
Turn-Off Delay Time	$t_{D(off)}$ — 12.0 — ns $R_{GEN} = 25\Omega$		$R_{GEN} = 25\Omega$				
Turn-Off Fall Time	t _f		6.3				

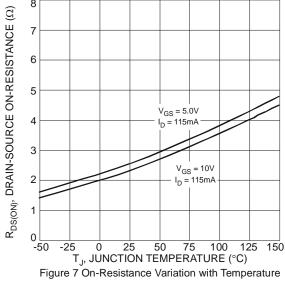
Notes:

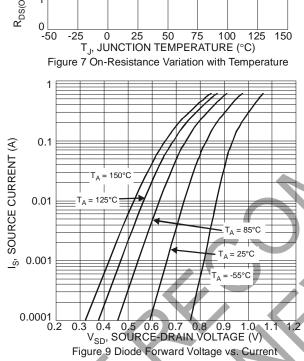
- 6. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
- 7. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad layout
- Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to production testing.











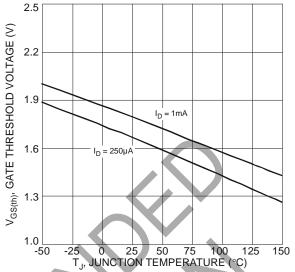
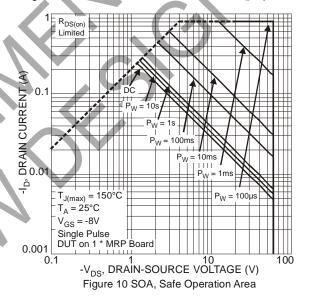
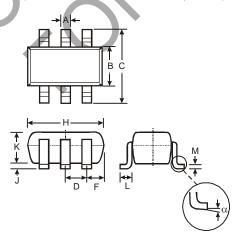


Figure 8 Gate Threshold Variation vs. Ambient Temperature



Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.

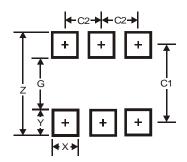


		SOT363						
Dim	Min	Max	Тур					
Α	0.10	0.30	0.25					
В	1.15	1.35	1.30					
С	2.00	2.20	2.10					
D		0.65 Typ						
F	0.40	0.45	0.425					
Н	1.80	2.20	2.15					
J	0	0.10	0.05					
K	0.90	1.00	1.00					
L	0.25	0.40	0.30					
М	0.10	0.22	0.11					
α	0°	8°	-					
All Dimensions in mm								



Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	2.5
G	1.3
Х	0.42
Υ	0.6
C1	1.9
C2	0.65

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